

ED060SC4

TECHNICAL SPECIFICATION

MODEL NO : ED060SC4

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Customer•s Confirmation

Customer

Date

Вy

PVI-s Confirmation

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Confirmed By

Prepared By

ED060SC4

OPRIME VIEW

Revision History

Rev.	Issued Date	Revised Contents
1.0	May,13,2008	New

TECHNICAL SPECIFICATION

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1. Application

The display is a TFT active matrix electrophoretic display, with associated interface and control logic, and a reference system design.

The 6" active area contains 800x600 pixels, and has full 1~4 bit display capabilities.

An integrated circuit containing interface, timing and control logic is supplied with each panel.

2. Features

- High contrast TFT electrophoretic
- ➢ 800x600 display
- ➢ High reflectance
- > Ultra wide viewing angle
- ➢ Ultra low power consumption
- > Pure reflective mode
- ➢ Bi-stable
- Commercial temperature range
- Landscape, portrait mode
- Antiglare hard-coated front-surface

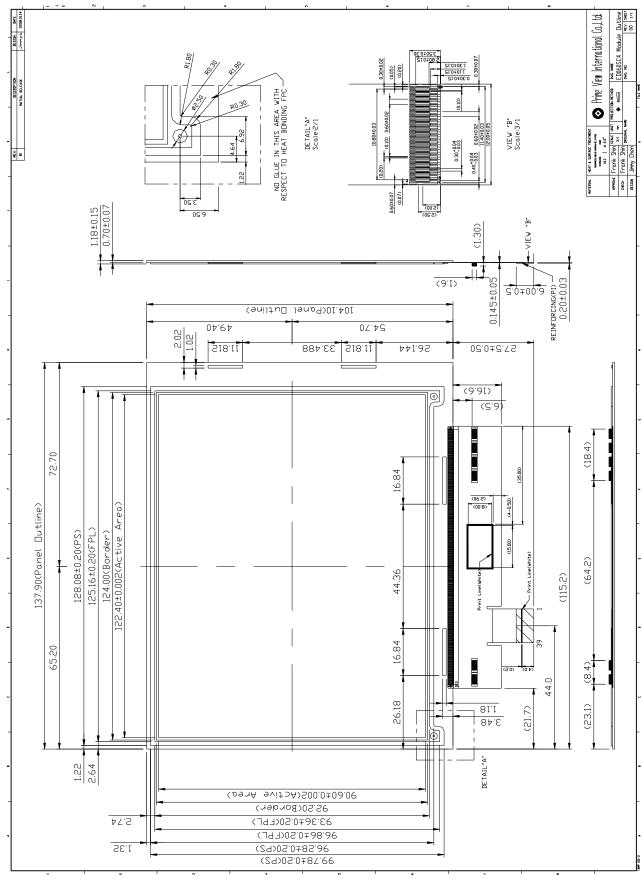
3. Mechanical Specifications

Parameter	Specifications	Unit	Remark
Screen Size	6.0 (4:3 diagonal)	Inch	
Display Resolution	800 (H) ×600(V)	Dot	
Active Area	122.4 (H)×90.6 (V)	mm	
Pixel Pitch	0.153 (H)×0.151 (V)	mm	
Pixel Configuration	Rectangle		
Outline Dimension	137.90(W)×104.10(H)×1.18(D) (panel area height) 137.90(W)×104.10(H)×2.0 (D) (FPC area height with capacitor)	mm	Panel height is measured without released film
Module Weight	35±5	g	



4. Mechanical Drawing of EPD Module

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5.Input/Ouput Interface

5-1)Pin Assignment

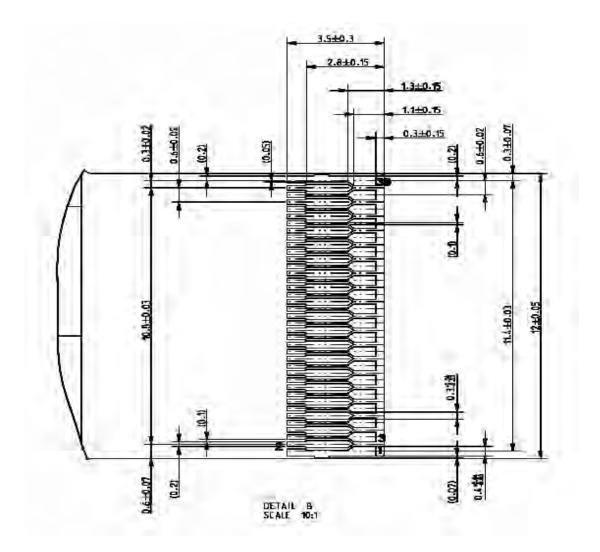
Pin #	Signal	Description
1	VNEG	Negative power supply source driver
2	VPOS	Positive power supply source driver
3	VSS	Ground
4	VDD	Digital power supply drivers
5	CL	Clock source driver
6	LE	Latch enable source driver
7	OE	Output enable source driver
8	SHR	Shift direction source driver
9	NC	NO Connection
10	NC	NO Connection
11	SPH	Start pulse source driver
12	D0	Data signal source driver
13	D1	Data signal source driver
14	D2	Data signal source driver
15	D3	Data signal source driver
16	D4	Data signal source driver
17	D5	Data signal source driver
18	D6	Data signal source driver
19	D7	Data signal source driver
20	NC	NO Connection
21	NC	NO Connection
22	VCOM	Common connection
23	GVDD	Positive power supply gate driver
24	GVEE	Negative power supply gate driver
25	NC	NO Connection
26	NC	NO Connection
27	GMODE2	Output mode selection gate driver
28	GMODE1	Output mode selection gate driver
29	RL	Shift direction gate driver
30	U1CE2	Cascade sequence 1st gate driver
31	U1CE1	Cascade sequence 1st gate driver
32	U1SPV	Start pulse 1st gate driver
33	U1CKV	Clock 1st gate driver
34	VBORDER	Border connection
35	U2CE2	Cascade sequence 2nd gate driver
36	U2CE1	Cascade sequence 2nd gate driver
37	U2SPV	Start pulse 2nd gate driver
38	U2CKV	Clock 2nd gate driver
39	NC	NO Connection



5-2)Panel Electrical Connection

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SERVICE	CONNECTOR	TYPE NUMBER	NUMBER OF PINS	MATING CONNECTOR
Interface	JST	39FXL-RSM1-S-H-TB	39	Copper foil 0.3mm pitch



6.Electrical Characteristics

6-1) Panel interface description

This panel is driven by ASIC PVI-6001A or "Apollo" display controller ASIC. See control product specification for details. **6-2) Panel DC characteristics**

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Signal ground	V _{SS}		-	0	-	V
	V_{DD}		3.0	3.3	3.6	V
Logic Voltage supply	I_{VDD}	$V_{DD}=3.3V$	-	0.55	1.1	mA
Coto Nogotivo oupply	GV_EE		-21	-20	-19	V
Gate Negative supply	GI _{EE}	GV_{EE} =-20V	-	1.3	3.9	mA
Gate Positive supply	GV_{DD}		21	22	23	V
Gale Positive supply	GI_DD	$GV_{DD} = 22V$	-	0.6	1.8	mA
Source Negative	V_{NEG}		-15.4	-15	-14.6	V
supply	I _{NEG}	$V_{NEG} = -15V$	-	18	36	mA
Source Positive	V _{POS}		14.6	15	15.4	V
supply	I _{POS}	$V_{POS} = 15V$	-	18	36	mA
Asymmetry source	V_{Asym}	V_{POS} + V_{NEG}	-80	0	80	mV
Common voltage	V _{COM}		-2.5	Adjusted	-1.0	V
	ICOM		-	0.2	-	mA
Maximum power panel	P _{MAX}		-	1000	1100	mW
Standby power panel	P _{STBY}		-	-	0.4	mW
Typical power panel	P _{TYP}		-	400	-	mW
Operating temperature			0	-	50	°C
Storage temperature			-25	-	70	°C
Maximum image update time at 25°C			-	1000	-	ms

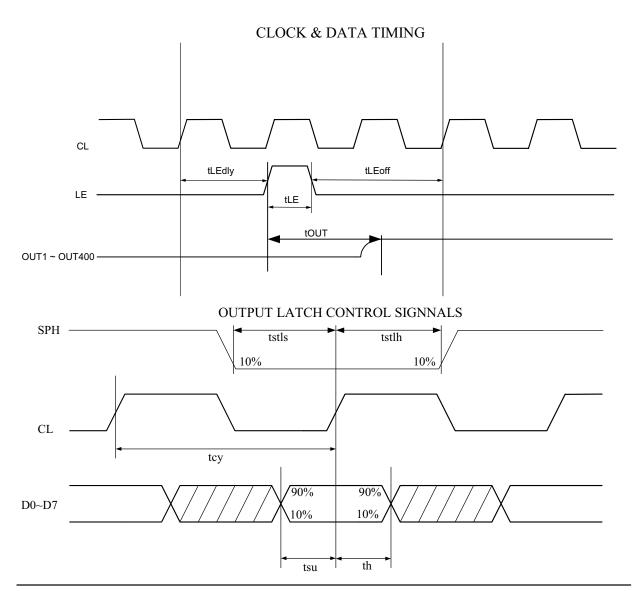
- The maximum power and maximum currents are specified for the worst case power consumption.

- The typical power is measured when "typical images" are displayed
- The standby power is the consumed power when the panel controller is in standby mode.
- The listed electrical/optical characteristics are only guaranteed under the controller & waveform provided by PVI.

6-3)Panel AC characteristics

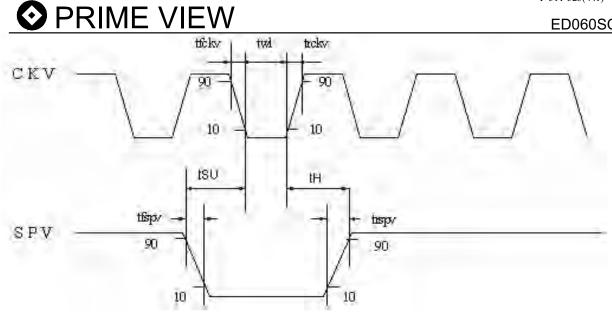
VDD=3.0V to 3.6V,	unless	otherwise	specified.	

Parameter	Symbol	Min.	Тур.	Max.	Unit	App Pin	
Clock frequency	fckv			200	kHz		
Minimum "L" clock pulse width	twL	0.5			us	CKV	
Clock rise time	trckv			100	ns		
Clock fall time	tfckv			100	ns	-	
Data setup time	tSU	100			ns	CKV,	
Data hold time	tH	100			ns	SPV	
Pulse rise time	trspv			100	ns	SPV	
Pulse fall time	tfspv			100	ns	JE V	
Clock CL cycle time	tcy	50		DC	ns		
D0 D7, SPH setup time	tsu	8			ns	-	
D0 D7, SPH hold time	th	1			ns	Below	
LE on delay time	tLEdly	40			ns	table	
LE high-level pulse width	tLEw	40			ns		
LE off delay time	tLEoff	40			Ns		









6-4) Power Consumption

Parameter	Symbol	Conditions	TYP	Max	Unit	Remark
Maximum panel power consumption during update.	-	-	-	1100	mW	
Power consumption in standby mode	-	-	-	0.4	mW	
Typical panel power	-	-	400	-	mW	

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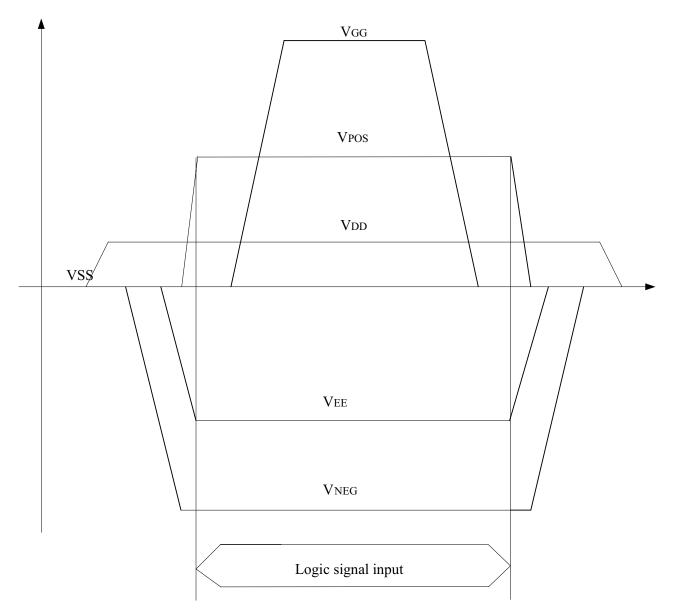
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7.Power on Sequence

1. VSS \rightarrow VDD \rightarrow VNEG \rightarrow VPOS (Source driver)

2. VEE \rightarrow VGG(Gate driver)

* Power on sequence's timing chart as blew :

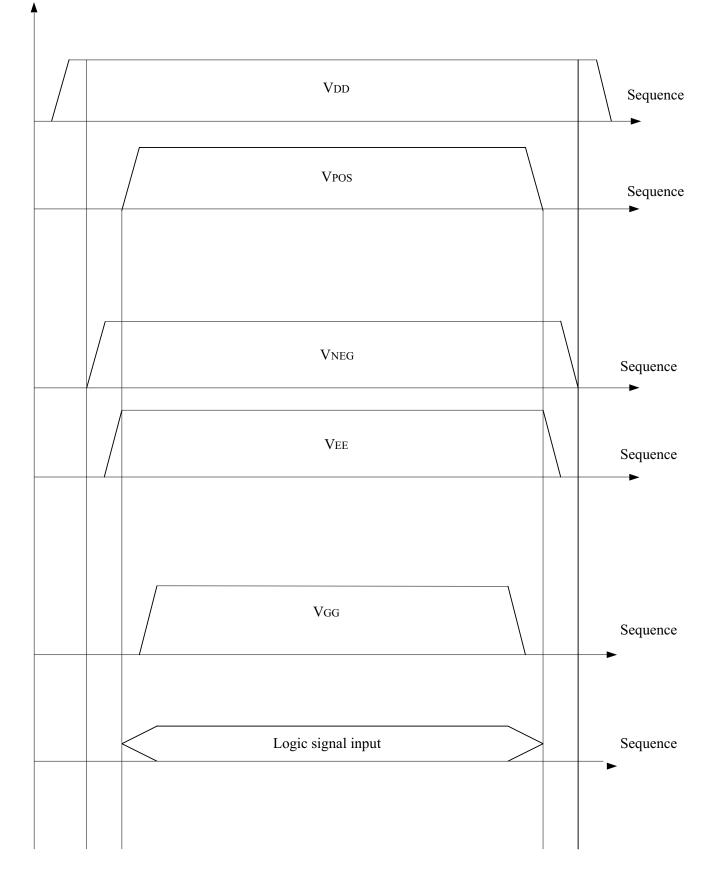




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* It is expanded as blew.



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8. Optical characteristics

8-1)Specifications

Measurements are made with that the illumination is under an angle of 30 degrees, the detection is perpendicular unless otherwise specified.

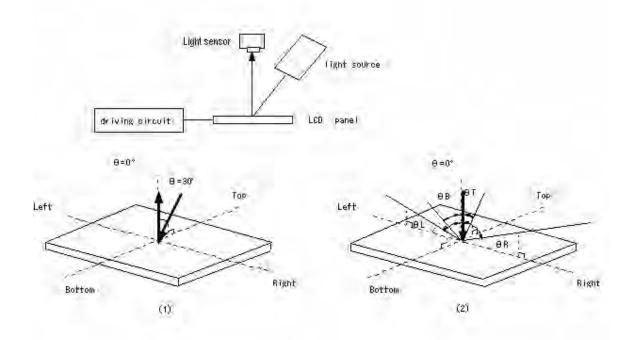
					T =	25°C	
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT	Note
R	Reflectance	White	30	35	-	%	8-1
Gn	N _{th} Grey Level	-	-	DS+(WS-DS)×n/(m-1)	-	L*	-
CR	Contrast Ratio	-	6	-	-		-
T _{update}	Update time	2~4-bit mode 1-bit mode		1000 540	-	ms ms	-

WS: White state, DS: Dark state, Gray state from Dark to White :DS $\$ G1 $\$ G2... $\$ Gn... $\$ Gm-2 $\$ WS m:4 $\$ 8 $\$ 16 when 2 $\$ 3 $\$ 4 bits mode

8-2)Definition of contrast ratio

The contrast ratio (CR) is the ratio between the reflectance in a full white area (Rl) and the reflectance in a dark area (Rd):

CR = Rl/Rd

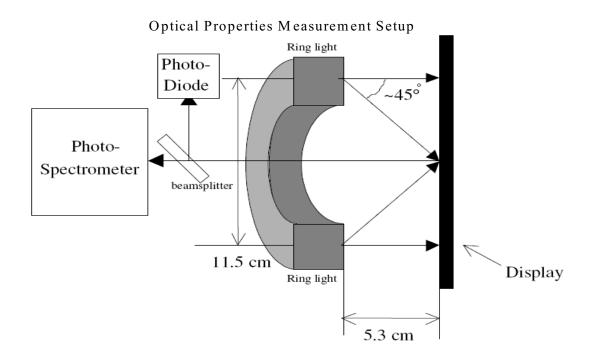




Note 8-1

Luminance meter: Eye-One Pro Spectrophotometer

Only reflectivity effect is counted when using above optical measurement with Eye-One Pro Spectrophotometer .Some scattering effect will be counted in actual visual conditions and the measurement data will be higher. A second optical measurement method and related data by which scattering effect counted are listed for reference.



Method	SYMBOL	PARAMETER	CONDITIONS	MIN.	Luminance meter	UNIT
Reflectivity only	R	Reflectance	White	30	Eye-One Pro Spectrophotometer	%
Reflectivity & scattering	R	Reflectance	White	35	EOTS	/0

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8-3)Reflection Ratio

The reflection ratio is expressed as:

 $R = Reflectance \ Factor_{white \ board} \quad x \quad (\ L_{center} \ / \ L_{white \ board})$

 L_{center} is the luminance measured at center in a white area (R=G=B=1). $L_{white board}$ is the luminance of a standard white board. Both are measured with equivalent illumination source (see figure 8.3). The viewing angle shall be no more than 2 degrees.

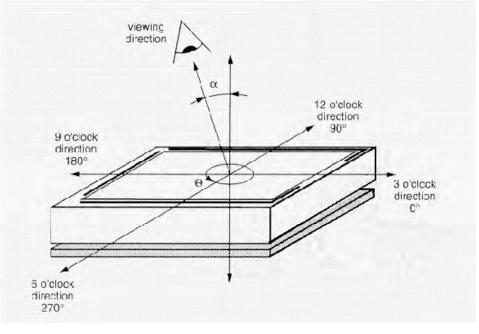


figure 8.3

 $\alpha = declination / \theta = azimuth$

8-4)Waveform

- Wave Form file should be available before panel delivery to customer.
- Wave Form file size should be 64Kb
- Ghosting quality is measured by the reflectance difference between specific area and surface.
 In MU and GU mode, within 3L*
 In GC mode, within 2L*

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9.HANDLING, SAFETY AND ENVIROMENTAL REQUIREMENTS

WARNING

The display glass may break when it is dropped or bumped on a hard surface. Handle with care. Should the display break, do not touch the electrophoretic material. In case of contact with electrophoretic material, wash with water and soap.

CAUTION

The display module should not be exposed to harmful gases, such as acid and alkali gases, which corrode electronics components.

Disassembling the display module can cause permanent damage and invalidates the warranty agreements.

Observe general precautions that are common to handling delicate electronic components. The glass can break and front surfaces can easily be damaged. Moreover the display is sensitive to static electricity and other rough environmental conditions.

Data sheet status					
Product	This data sheet contains final product specifications.				
specification					
Limiting valu	ies				
Limiting valu	es given are in accordance with the Absolute Maximum Rating System (IEC				
134). Stress a	bove one or more of the limiting values may cause permanent damage to the				
device. These	are stress ratings only and operation of the device at these or at any other				
conditions abo	ove those given in the Characteristics sections of the specification is not implied.				
Exposure to limiting values for extended periods may affect device reliability.					
Application information					
Where application information is given, it is advisory and does not form part of the					
specification.					



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10.11	eliability test		· · · · ·	
	TEST	CONDITION	METHOD	REMARK
1	High-Temperature Operation	T = +50°C, RH = 30% for 240 hrs	IEC 60 068-2-2Bp	At the end of the test, electric, mechanical, and optical specifications shall be satisfied.
2	Low-Temperature Operation	T = 0°C for 240 hrs	IEC 60 068-2-2Ab	At the end of the test, electric, mechanical, and optical specifications shall be satisfied.
3	High-Temperature Functional	T = +60°C, RH=26% for 240 hrs	IEC 60 068-2-2Bp	At the end of the test, electric, mechanical, specifications shall be satisfied.
4	Low-Temperature Functional	T = -10°C for 240 hrs	IEC 60 068-2-2Ab	At the end of the test, electric, mechanical, specifications shall be satisfied.
5	High-Temperature Storage	T = +70°C, RH=23% for 240 hrs	IEC 60 068-2-2Bp	At the end of the test, electric, mechanical, and optical specifications shall be satisfied.
6	Low-Temperature Storage	T = -25°C for 240 hrs	IEC 60 068-2-1Ab	At the end of the test, electric, mechanical, and optical specifications shall be satisfied.
7	High-Temperature, High-Humidity Operation	T = +40°C, RH = 90% for 168 hrs	IEC 60 068-2-3CA	At the end of the test, electric, mechanical, specifications shall be satisfied.
8	High Temperature, High- Humidity Storage	T = +60℃, RH=80% for 240hrs	IEC 60 068-2-3CA	At the end of the test, electric, mechanical, specifications shall be satisfied.
9	Temperature Cycle	1 cycle:[-25℃ 30min]→[+70 ℃ 30 min] : 100 cycles	IEC 60 068-2-14	At the end of the test, electric, mechanical, specifications shall be satisfied.
10	UV exposure Resistance	765 W/m2 for 168hrs,40 ℃	IEC60 068-2-5Sa	Optical characteristics shall be satisfied.
11	Package Vibration	1.04G, Frequency: 10~500Hz Direction: X,Y,Z Duration: 1 hours in each direction	Full packed for shipment	At the end of the test, electric, mechanical, and optical specifications shall be satisfied.
12	Package Drop Impact	Drop from height of 122 cm on concrete surface. Drop sequence: 1 corner, 3 edges, 6 faces One drop for each.	full packed for shipment	At the end of the test, electric, mechanical, and optical specifications shall be satisfied.
13	Electrostatic Effect (non-operating)	Machine model +/- 250V, 0 Ω , 200pF	IEC 62179, IEC 62180	At the end of the test, electric, mechanical, specifications shall be satisfied.
14	Altitude test Operation	700hPa(= 3000m) 48Hr		At the end of the test, electric, mechanical, specifications shall be satisfied.

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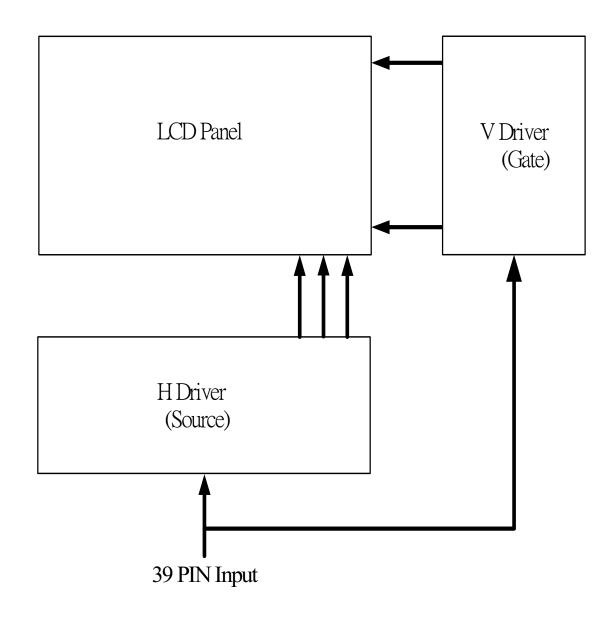
15	Altitude test Storage	260hPa (= 10000m) 48Hr	At the end of the test, electric, mechanical, specifications shall be satisfied.
16	Stylus Tapping	POLYACETAL Pen: Top R0.4mm Load: 300gf Speed: 5times/sec Total 13,500times,	Pass criteria – no glass breakage or damage to microcapsules.

Actual EMC level to be measured on customer application

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12.Packing

